



SPECIFICATION GRAPHIC TYPE DOT MATRIX LCD MODULE



ITEM NUMBER: FDCG12232G-FSWFBW-51AN

ESTABLISHED DATE: 1999.06 INITIAL ISSUED DATE: 2002.02

DATASHEET VERSION: 2008 VERSION

ISSUED BY: 魏蘇京 CHECKED BY: 经特点APPROVED BY: 经特点

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STANDARD DOC.

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FORDATA's 2006 version logo. FORDATA is an integrated manufacturer of flat panel display (FPD). FORDATA supplies TN, HTN, STN, FSTN monochrome LCD panel; COB, COG, TAB LCD module; and all kinds of LED backlight.

classic mono LCDs



FAST RESPONSE TIME

This icon on the cover indicates the product is with high response speed; Otherwise not.



PROTECTION CIRCUIT

This icon on the cover indicates the product is with protection circuit; Otherwise not.



HIGH CONTRAST

This icon on the cover indicates the product is with high contrast; Otherwise not.



LONG LIFE VERSION

This icon on the cover indicates the product is long life version (over 9K hours guaranteed); Otherwise not.



WIDE VIEWING SCOPE

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OPERATION TEMPERATURE RANGE

This icon on the cover indicates the operating temperature range (X-Y).



3TIMEs 100% QC EXAMINATION

This icon on the cover indicates the product has passed FORDATA's thrice 100% QC. Otherwise not.



TWICE SELECTION OF LED MATERIALS

This icon on the cover indicates the LED had passed FORDATA's twice strict selection which promises the product's identical color and brightness; Otherwise not.



VIcm = 3.0V

This icon on the cover indicates the product can work at 3.0V exactly; otherwise not.



N SERIES TECHNOLOGY (2008 developed)

FORDATA adopts new structure, new craft, new technology and new materials inside both LCD module and LCD panel to improve the "RainBow"



REVISION RECORD

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NO.	DATE	DESCRIPTION	ITEM	PAGE	APPROVED
1	2005.05	INITIAL ISSUED	ALL	ALL	LU BOO
2	2007.04	Added further information of LED backlight	4	4/20	State .
3	2008.01	Adopt logos on the cover for fast reference	-	Cover	Style .
4	2008.10	Deleted "N = No Ic" from CODE2	-	Code System	Style.
5	2008.10	Added CODE "B" for DFSTN version in CODE7	-	Code System	State.



CODE SYSTEM STANDARD COB

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1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
FD	С	С	08	01	A	F	L	Y	Y	В	w	5	2	L	E

No.	REMARKS	DESCRIPTION
1	COMPANY ABBRAVIATED	FD = FORDATA
2	IC packing	C = Chip On Board G = Chip On Glass T = TAB
3	LCM type	C = Character G = Graphic
4	Chyaracter	08, 10, 12, 16, 20, 24, 40, = Character number Per line
4	Graphic	80, 100, 120, 122, 128, 160 = Row Dots Quantity
-	Character	01, 02, 04, = Character Lines
5	Graphic	32, 64, 80, 128, 160 =Column Dots Quantity
6	Serial Number	A~Z
7	Polarizer type	R = Positive Reflective M = Positive Transmissive N = Negative Transmissive E = Negative, Transflective B = Negative, Dual optical compensation (for FSTN type only)
8	Backlight type	N = No Backlight S = Edge Type LED Backlight L = Array Type LED Backlight S = Edge Type LED Backlight E = EL backlight without Invertor F = EL backlight with Invertor C = CCFL backlight without Invertor T = CCFL backlight with Invertor
9	Backlight color	N = No BacklightY = Yellow-GreenW = WhiteR = RedA = AmberC = Blue-GreenB = BlueG = Green
10	LCD panel type	T = TN $H = HTN$ $Y = Yellow-Green STN$ $G = Gray STN$ $B = Blue STN$ $F = FSTN$
11	Viewing angle	B = Bottom 6:00 T = Top 12:00 R = Right 3:00 L = Left 9:00
12	Operation temperature range	$S = 0^{\circ}C \sim 50^{\circ}C$ (Single Supply Voltage) $D = 0^{\circ}C \sim 50^{\circ}C$ (Dual Supply Voltage) $W = -20^{\circ}C \sim 70^{\circ}C$ (Single Supply Voltage) $H = -20^{\circ}C \sim 70^{\circ}C$ (Dual Supply Voltage) $T = -30^{\circ}C \sim 80^{\circ}C$ (Single Supply Voltage) $E = -30^{\circ}C \sim 80^{\circ}C$ (Dual Supply Voltage)
13	Driving Voltage	1: VIcm = 3.0V, No / EL / CCFL Backlight or VIcm = 3.0V, VIed = LED voltage, (Via AK) 2: VIcm = 3.6V, VIed = 5.0V (Not via AK) 3: VIcm = 3.6V, VIed = LED voltage, (Not via AK) 4: VIcm = 5.0V, VIed = LED voltage, (Not via AK) 5: VIcm = 5.0V, VIed = 5.0V (Not via AK) 6: VIcm = 5.0V, No / EL / CCFL Backlight or VIcm = 5.0V, VIed = LED voltage, (Via AK) 7: VIcm = 3.6V, VIed = LED voltage, (Via AK) 8: VIcm = 3.0V, VIed = 5.0V 9: VIcm = 3.0V, VIed = LED voltage, (Not via AK)
14	Backlight Connect Method	0 = PIN1 LED-, PIN2 LED+ 1 = PIN15(17/19) LED+, PIN16(18/20) LED- 2 = PIN15(17/19) LED-, PIN16(18/20) LED+ 3 = PIN15(17/19) LED+, PIN16(18/20) NC 4 = PIN15(17/19) NC, PIN16(18/20) LED+ 5 = PINA LED+, PINK LED- 6 = No / EL / CCFL Backlight
15	IC Manufacturer	X = SAMSUNG L = SUNPLUS S = SITRONIX T = TOSHIBA E = EPSON H = HOLTEK Q = ASLIC N = CIMTEK P = PRINCETON
16	Font Set	R = English - Russia E = English - Japanese U = English - Europe H = English - Hebrew K = English - Europe N = NO FONT SET

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1. GENERAL SPECIFICATIONS

ITEM	NOMINAL DIMENSIONS / AVAILABLE OPTIONS
DISPLAY FORMAT	122 X 32 DOT MATRIX
LCD PANEL OPTIONS	FSTN (Silver-gray color)
POLARIZER OPTIONS	Positive, Transflective
BACKLIGHT OPTIONS	Edge type LED backlight (White color)
VIEWING ANGLE OPTIONS	6:00 (Bottom)
TEMPERATURE RANGE OPTIONS	Wide temperature range (- 20 $^{\circ}$ C ~ 70 $^{\circ}$ C)
CONTROLLERIC	AVANT
DISPLAY DUTY	1/32
DRIVING BIAS	1/7

2. MECHANICAL SPECIFICATIONS

OVERALL SIZE	LED backlight version: 84.0 x 44.0 x max 15.0						
VIEWING AREA	64.0W x 17.9H	mm HOLE-HOLE 76.0W x 36.0H					
DOT SIZE	0.40W x 0.45H	mm	DOT PITCH	0.04W x 0.04H	mm		
WEIGHT (EL BKL)	86.0	g	WEIGHT (LED BKL)	105.0	g		

3. ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	CONDITION	MIN	MAX	UNIT
POWER SUPPLY (LOGIC)	Vdd	25°C	-0.3	7.0	V
POWER SUPPLY (LCD)	V0	25°C	Vdd -13.5	Vdd +0.3	V
INPUT VOLTAGE	Vin	25°C	-0.3	Vdd +0.3	V
OPERATING TEMPERATURE	Vopr		-20	70	°C
STORAGE TEMPERATURE	Vstg		-30	80	°C

4. ELECTRONICAL CHARACTERISTIC*

1751	CVMDOL	CONDITION	S	FANDA	RD	
ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
Input voltage	Vdd	+5V	4.7	5.0	5.5	V
Supply current	ldd	Vdd=5V		0.9		mA
		-20°C	4.90		5.60	
Recommended LCD driving		0°C	4.75		5.45	
voltage for normal temp.	Vdd - V0	25 [°] C	4.60	4.80	5.30	V
Version module		50°C	4.45		5.15	
		70°C	4.25		4.95	
LED forward voltage	Vf	25°C	2.9		3.4	V
LED forward current	lf	25 [°] C		15	20	mA
LED reverse Current	lr	25°C		10		μA
LED color range	X coordinate	25°C If = 15mA	0.25		0.28	
LED Color range	Y coordinate	25°C If = 15mA	0.26		0.29	
LED illuminance (Without LCD)	Lv	25°C If = 15mA				cd/m²
LED life time		25°C If = 15mA	9K**			Hours

^{*} The above data are for reference only.

^{**} The warranty period of FORDATA LCD module is 1YEAR counted from the date shown on the label of products.

^{**} If you wanted to drive the LED BKL uninterruptedly exceed 12hours/day, you are not suggested this version



SPEC.

MODE NO. FDCG12232G-FSWFBW-51AN

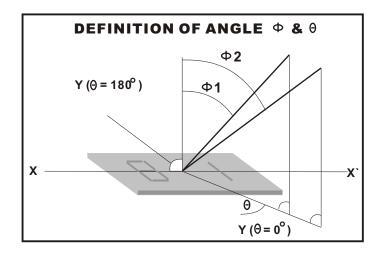
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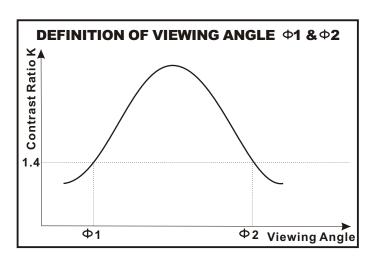
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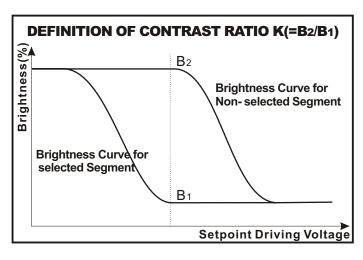
5. OPTICAL CHARACTERISTICS

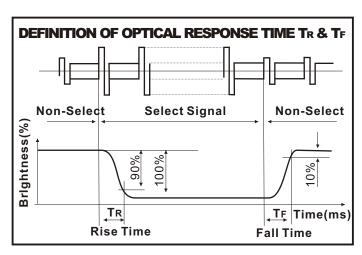
FOR TN TYPE LCD MODULE (TA=25°C, Vdd=5.0V ± 0.25V)								
ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT		
VIEWING ANGLE	Ф2-Ф 1	K=4	30			deg		
VIEWING ANGLE	Θ	N=4	25			ueg		
CONTRAST RATIO	K			2				
RESPONSE TIME(RISE)	T R			120	150	ms		
RESPONSE TIME(FALL)	TF			120	150	ms		

FOR STN TYPE LCD MODULE (TA=25 °C, Vdd=5.0V ± 0.25V)								
ITEM SYMBOL CONDITION MIN TYP MAX UNIT								
VIEWING ANGLE	Ф2-Ф 1	17 – 4	40			deg		
VIEWING ANGLE	Θ	K=4	60			ueg		
CONTRAST RATIO	K			6				
RESPONSE TIME(RISE)	T R			150	250	ms		
RESPONSE TIME(FALL)	TF			150	250	ms		









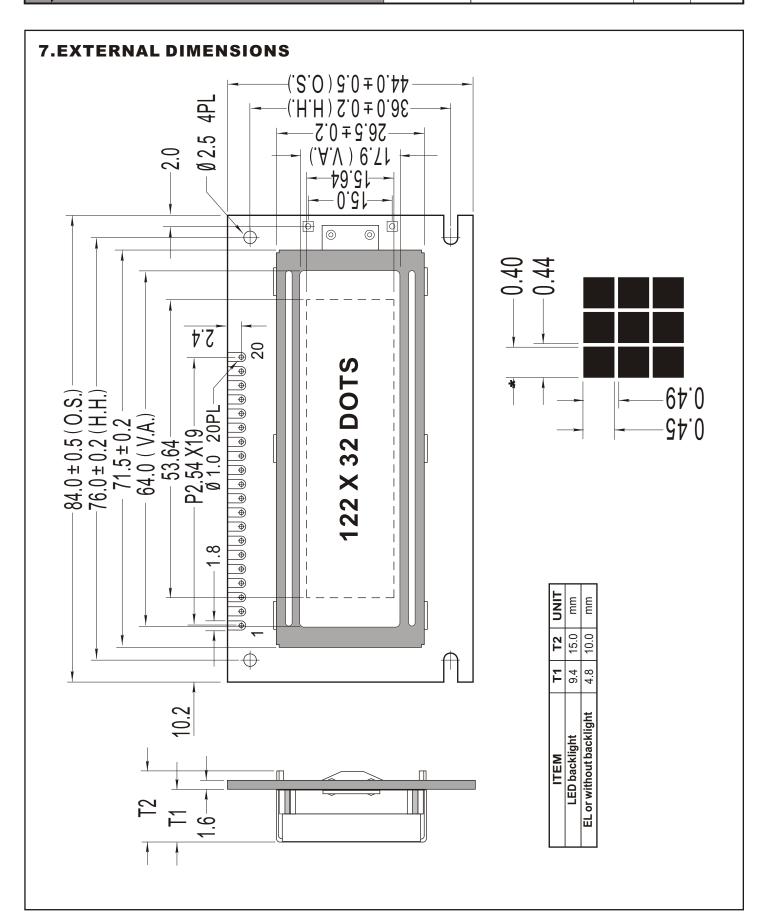


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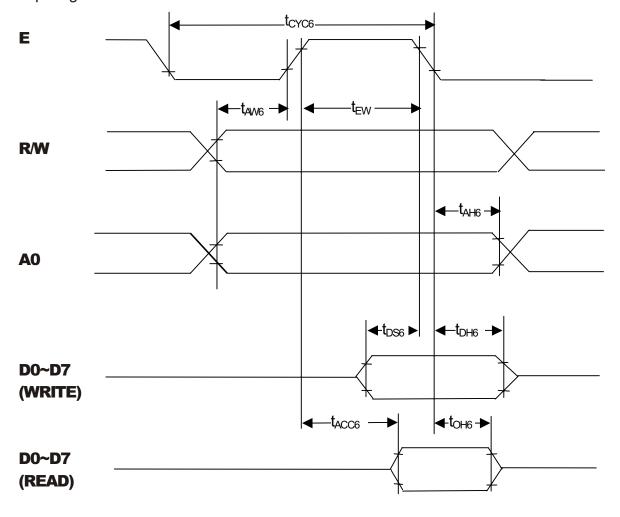
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6. AC CHARACTERISTIC

 V_{dd} =5.0V±10%, V_{SS} =0V, T_a = -20 ~ +75 $^{\circ}$ C

Parameter		Symbol	Min	Max	Condition	Unit
Address set up time		t _{AW6}	20	_		ns
Address hold tin	ne	t _{AH6}	10	_		ns
System cycle tin	ne	t _{CYC6}	1000	_		ns
E pulse width	Read	t	100	_	_	ns
E puise widu	Write	t _{EW}	80	_		ns
Data set up time)	t _{DS6}	80	_		ns
Data hold time		t _{DH6}	10	_		ns
Access time		t _{ACC6}	_	90	C.=100pE	ns
Output disable time		t _{OH6}	10	60	C _L =100pF	ns

^{*}Input signal rise time and fall time are less than 15ns.





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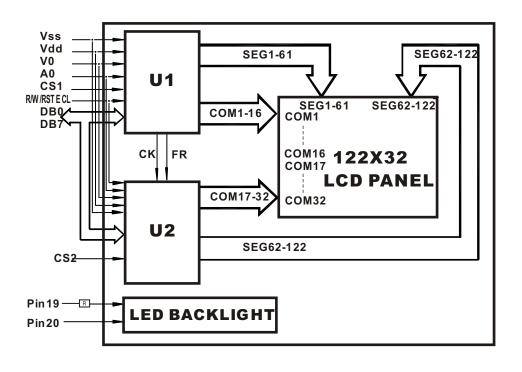
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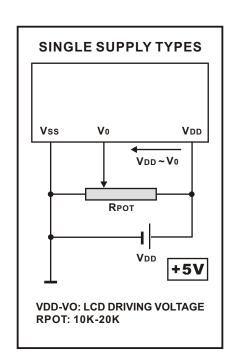
8. PIN ASSIGNMENT

PIN NO.	SYMBOL	FUN	FUNCTION			
1	Vss		0V			
2	Vdd	Power Supply	Power Supply +5V			
3	V0		Contrast Adjust			
4	A0	H/L H: Data; L	: Instruction code			
5	CS1	Chip 1 E	nable signal			
6	CS2	Chip 2 E	nable signal			
7	CL	Clock Ir	nput (2K Hz)			
8	Е	Enal	ole Signal			
9	R/W	Rea	d / Write			
10	DB0	Dat	a Bit 0			
11	DB1	Dat	a Bit 1			
12	DB2	Dat	a Bit 2			
13	DB3	Dat	a Bit 3			
14	DB4	Dat	a Bit 4			
15	DB5	Dat	a Bit 5			
16	DB6	Dat	a Bit 6			
17	DB7	Data Bit 7				
18	RST	Rese	Reset Signal			
19	LED+	Anode of LED Unit		Anode of LED Unit		+5V
20	LED-	Cathode	0V			

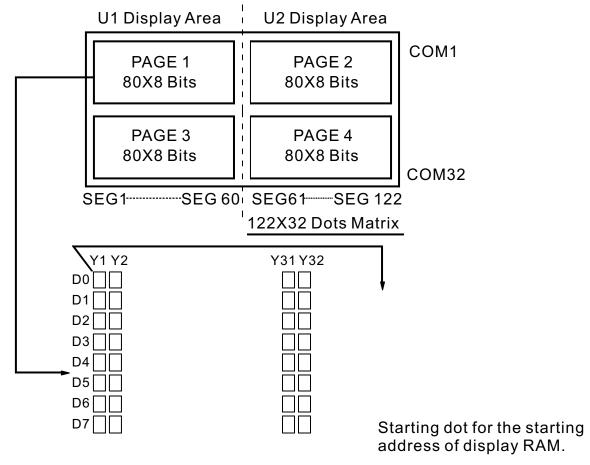
9.1 . BLOCK DIAGRAM



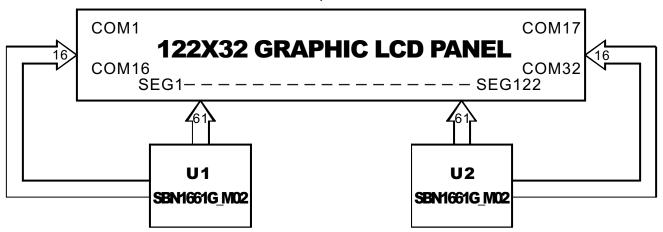
9.2. POWER SUPPLY



10. RELATION BETWEEN DISPLAY PATTERN AND DRIVERS



Each segment driver has 4 pages RAM, and each page has 80x8 bits RAM. D0~D7 are 8 bits transmitted data, where D0 is LSB and D7 is MSB.





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11. INSTRUCTIONCODE

Instruction	A0	RW	D7	D6	D5	D4	D3	D2	D1	D0	Desc	cription
Display on/off	0	0	1	0	1	0	1	1	1	0/1	Whole di 1: on 0:	•
Display Start line	0	0	1	1	0	DISPL	AY STA	RT AD	DRESS	3 (1-31)	Determine the correspond to	•
Page address set	0	0	1	0	1	1	1	0		age -3)	Set the page of	f disp data RAM
Column address set	0	0	0	C	olur	nn ac	ddres	ss(0-	79)		Set the column data RAM	address of disp
Status read	0	1	B∪ 0>	ADC	0 X / 0 F F	R E S E T	0	0	0	0	ADC 0: coun 1; clock ON/OFF 0: dis	y 1: working ter clockwise xwise output p on 1: disp off ormal 1: reset
Write display data	1	0				Write	data				Write data to disp RAM	Access the
Read display data	1	1				Read	d data	ì			Read data from disp RAM	predetermind address of the disp RAM
ADC select	0	0	1	0	1	0	0	0	0	0/1	Determine the of the disp RA 0: clockwise o 1: counter clockwise o	utput
Static drive on/off	0	0	1	0	1	0	0	1	0	0/1	driving 1: stati	namic or static c driving amic driving
Duty ratio select	0	0	1	0	1	0	1	0	0	0/1	Select the 0: 1/16	duty ratio 1: 1/32
Read Modify write	0	0	1	1	1	0	0	0	0	0	Increment the cregister when we change when r	•
END	0	0	1	1	1	0	1	1	1	0	Release from the Write mode	ne Read Modify
Reset	0	0	1	1	1	0	0	0	1	0	Set the display register to 1st register to 3.	/ start line line, page add
Power save (dual command)	0	0 0	1	0 0	1 1	0 0	1 0	1	1 0	0	Set the power selecting disp driving on.	save mode by off and static



12. INSTRUCTION DESCRIPTION

A. Display On / Off

This is instruction executes whole display On/Off no relation with the data in the Display Data RAM and internal conditions.

		R/W								
Code	0	0	1	0	1	0	1	1	1	D

D 0: Display On 1: Display Off

When the static driving mode is selected (static drive On) in display Off status, the internal circuits put on the power save mode.

B. Display Start Line

This instruction set the line address. The selected line in the Display Data RAM correspond to the COM0 which display at the top of LCD panel

The display area is set automatically from the selected line to the line which increased the one or page switching are available by this instruction.

	_	R/W		_	_		_			_
Code	0	0	1	1	0	A 4	A 3	A 2	A 1	A 0

A4	А3	A2	A1	A0	Line Address
0	0	0	0	0	0
				1	1
1	1	1	1	0	1E
1	1	1	1	1	1F



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C. Page Address Set

When MPU access the display Data RAM, the page address corresponded to the row address must be selected.

The access in the display Data RAM is available by setting the page and column address. The display is no change when the page address is changed.

Α0 R/W D7 D6 D₅ D4 D3 D2 D₀ D1 Code 0 0 1 0 1 1 1 0 **A1** Α0

A1	A0	Page
0	0	0
0	1	1
1	0	2
1	1	3

D. Column Address Set

This instruction set the column address in the Display Data RAM.

When the MPU access the Display Data RAM continuously, the column address increase 1 automatically, therefore, the MPU can access the data only without address setting. The increment of the column address is stopped by the address of 50H automatically, but the page address is no change even if the column address increase to 50H and stop.

Α0 R/W D7 D6 D5 D4 D3 D2 **D**1 D0 0 **A6 A5 A4 A3** A2 **A1** A0 Code

A6	A5	A4	A3	A2	A1	A0	ColumnAdd.
0	0	0	0	0	0	0	0
0	0	0	0	0	0	1	1
1	0	0	1	1	1	0	4E
1	0	0	1	1	1	1	4F



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E. Status Read

This instruction read out the internal status.

A0 R/W D7 D6 D5 D4 D3 D2 D1 D₀ **BUSY** ADC ON/OFF **RESET** 0 0 0 0 0 Code

BUSY: BUSY=1 indicate the operating or the Reset cycle

The instruction can be input after the BUSY status change to 0.

ADC: Indicate the output correspondence of column (segment) address and segment driver.

0: Counter clockwise Output (Inverse)

Column Address 79 - n - Segment Driver n

(Normal) 1: Clockwise Output

Column Address n Segment Driver n

ON/OF: Indicate the whole display On / Off status.

0: Whole Display On

1: Whole Display Off

(**Note**) The data 0 = On and 1 = Off of Display On/ Off status read out is inverted with the Display On/Off instruction data of 1 = On and 0 = Off

RESET: Indicate the initialization period by reset instruction.

0:

1: Initialization Period

F. Write Display Data

This instruction write the 8-bit data on the data bus into the Display RAM. The column (segment) address increase 1 automatically when writing, therefore, the MPU can write the 8-bit data into the Display Data RAM without address setting.

	Α0	R/W	D7	D6	D5	D4	D3	D2	D1	D0
61SEG	1	0				Write	Data			



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G. Read Display Data

This instruction read out the 8-bit data from Display Data RAM which addressed by the column and page address. In case of the Read Modify Write Mode is Off, the column address increase 1 automatically after each read out, therefore, the MPU can read out the 8-bit data from the Display Data RAM continuously without address setting.

	Α0	R/W	D7	D6	D5	D4	D3	D2	D1	D0
Code	1	1				Read	Data			

H. ADC Select

This instruction set the correspondence of column address in the Display Data RAM and segment driver out. Therefore, the order fo segment output can be changed by the software, and no restriction of the LSI placement against the LCD panel.

	Α0	R/W	D7	D6	D5	D4	D3	D2	D1	D0	
Code	0	0	1	0	1	0	0	0	0	D	

D 0: Clockwise Output (Inverse) 1: Counter Clockwise Output (Normal)

Static Drive On/ Off

This instruction executes the all common output terms on and whole display on obligatory

	Α0	R/W	D7	D6	D5	D4	D3	D2	D1	D0
Code	0	0	1	0	1	0	0	1	0	D
					`	mal O _l		,	On)	

When the Display Off mode is selected (Display Off) in Static Driver On status, the internal circuits put on the power save mode.



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J. Duty ratio Select

This instruction set the LCD driving duty ratio.

A0 R/W D7 D6 D5 D4 D3 D2 D1 D0 Code 0 0 1 0 1 1 0 0 0 D

> 0:1/16 Duty D 1: 1/32 Duty

K. Read Modify Write

After this instruction is executed, the column address increase 1 automatically when Display Data Write Instruction execution, but the address is not changed when the Display Data Read Instruction execution.

This status continues during End instruction execution. When the End instruction is entered the column address back to the address where Read Modify Write instruction entering. By this function, the load of MPU for example cyclic data writing operation like as cursor blink etc., can be reduced.

	_	R/W		_	_		_			_
Code	0	0	1	1	1	0	0	0	0	0

(Note) During the Read Modify Write mode, any instruction except Column Address Set can be executed.

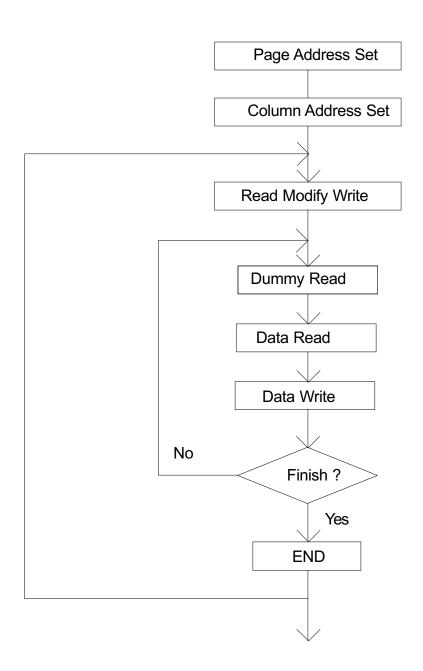


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L. Sequence of cursor display





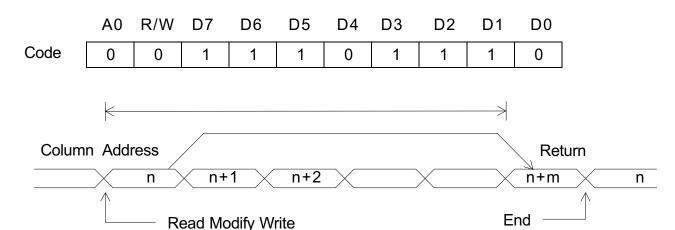
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M. End

This instruction release the Read Modify Write mode and the column address back to the address where the Read Modify Write mode setting.



N. Reset

This instruction executes the following initialization.

INitialization

- 1) Set the first line in the Display Start Line Register.
- 2) Set the page 3 in the Page Register.

In this time, there are no influence to the Display Data RAM.

	Α0	R/W	D7	D6	D5	D4	D3	D2	D1	D0
Code	0	0	1	1	1	0	0	0	1	0

(Note) The initialization when the power terms on can not be executed by Reset instruction

O. Power Save (Dual Command)

When both of Display Off and Static Drive On are executed, the internal put on the power save mode and the current consumption is reduced as same as stand by current. The internal status in this mode are as following:

- 1) Stop the LCD driving. Segment and Common drivers output Vdd level
- 2) Stop the oscillation or inhibit the external clock input
- 3) Keeping the display data and operating mode.

The power save mode is released by Display on or static drive off instruction.



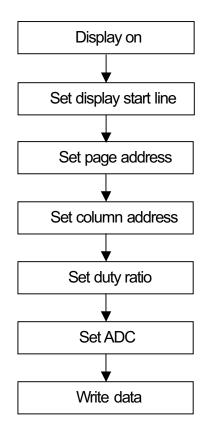
PRODUCT SPEC.

MODE NO. FDCG12232G-FSWFBW-51AN

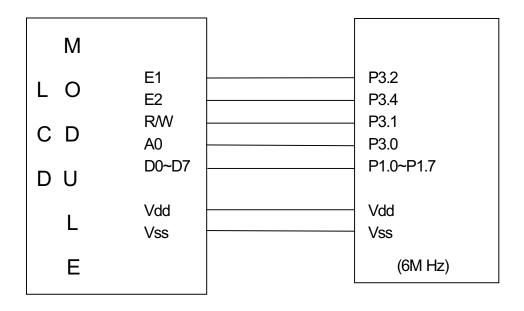
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13. APPLICATION EXAMPLE

Application Flowchart



Application Circuit



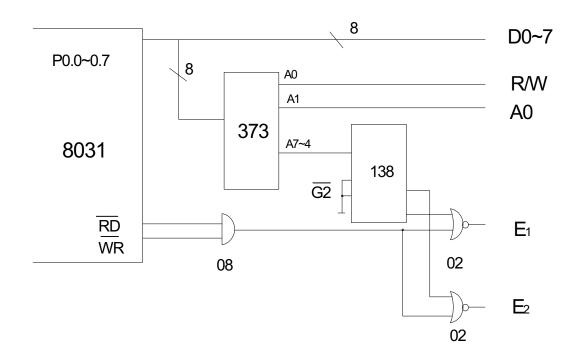


PRODUCT SPEC.

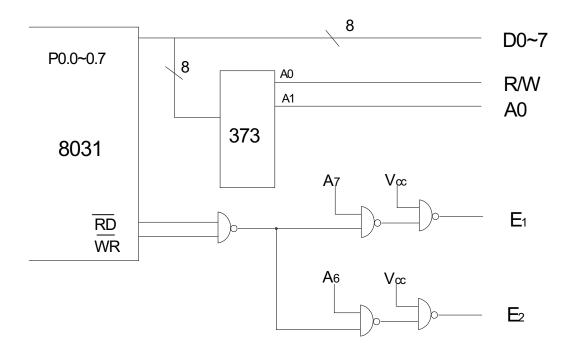
MODE NO. FDCG12232G-FSWFBW-51AN

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Application Circuit 1



Application Circuit 2





SPEC.

MODE NO. FDCG12232G-FSWFBW-51AN

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14. PACKING DETAIL

WITH LED BKL

45 PCS/BOX **10 BOXES/CARTON 450 PCS/CARTON** 20.00 KGS/CTN(G.W.) WITHOUT LED BKL 45 PCS/BOX 10 BOXES/CARTON 450 PCS/CARTON 18.00 KGS/CTN(G.W.)

NOTE

- 1. The weight is estimated for reference only.
- 2. Packing detail may be changed without notice.

